

## CONFIG (address:0x2007, mask:0x21FF, default:0x21FF)

### WDTE -- Watchdog Timer Enable bit (bitmask:0x0004)

WDTE = OFF	0x3FFB	WDT disabled.
WDTE = ON	0x3FFF	WDT enabled.

### PWRTE -- Power-up Timer Enable bit (bitmask:0x0008)

PWRTE = ON	0x3FF7	PWRT enabled.
PWRTE = OFF	0x3FFF	PWRT disabled.

### FOSC -- Oscillator Selection bits (bitmask:0x0013)

FOSC = LP	0x3FEC	LP oscillator: Low-power crystal on RA6/OSC2/CLKOUT and RA7/OSC1/CLKIN.
FOSC = XT	0x3FED	XT oscillator: Crystal/resonator on RA6/OSC2/CLKOUT and RA7/OSC1/CLKIN.
FOSC = HS	0x3FEE	HS oscillator: High-speed crystal/resonator on RA6/OSC2/CLKOUT and RA7/OSC1/CLKIN.
FOSC = ECIO	0x3FEF	EC: I/O function on RA6/OSC2/CLKOUT pin, CLKIN on RA7/OSC1/CLKIN.
FOSC = INTOSCIO	0x3FF0	INTOSC oscillator: I/O function on RA6/OSC2/CLKOUT pin, I/O function on RA7/OSC1/CLKIN.
FOSC = INTOSCCLK	0x3FFD	INTOSC oscillator: CLKOUT function on RA6/OSC2/CLKOUT pin, I/O function on RA7/OSC1/CLKIN.
FOSC = EXTRCIO	0x3FFE	RC oscillator: I/O function on RA6/OSC2/CLKOUT pin, Resistor and Capacitor on RA7/OSC1/CLKIN.
FOSC = EXTRCCLK	0x3FFF	RC oscillator: CLKOUT function on RA6/OSC2/CLKOUT pin, Resistor and Capacitor on RA7/OSC1/CLKIN.

### MCLRE -- RA5/MCLR/VPP Pin Function Select bit (bitmask:0x0020)

MCLRE = OFF	0x3FDF	RA5/MCLR/VPP pin function is digital input, MCLR internally tied to VDD.
MCLRE = ON	0x3FFF	RA5/MCLR/VPP pin function is MCLR.

### BOREN -- Brown-out Detect Enable bit (bitmask:0x0040)

BOREN = OFF	0x3FBF	BOD disabled.
BOREN = ON	0x3FFF	BOD enabled.

### LVP -- Low-Voltage Programming Enable bit (bitmask:0x0080)

LVP = OFF	0x3F7F	RB4/PGM pin has digital I/O function, HV on MCLR must be used for programming.
LVP = ON	0x3FFF	RB4/PGM pin has PGM function, low-voltage programming enabled.

### CPD -- Data EE Memory Code Protection bit (bitmask:0x0100)

CPD = ON	0x3EFF	Data memory code-protected.
CPD = OFF	0x3FFF	Data memory code protection off.

### CP -- Flash Program Memory Code Protection bit (bitmask:0x2000)

CP = ON	0x1FFF	0000h to 07FFh code-protected.
CP = OFF	0x3FFF	Code protection off.