

TOP_CPU-MMU_fuer_VHDL

| To | Direction | Location | Fitter Location |
|-------------|-----------|----------|-----------------|
| A14 | Input | PIN_74 | PIN_74 |
| A15 | Input | PIN_73 | PIN_73 |
| A_7_0[7] | Input | PIN_75 | PIN_75 |
| A_7_0[6] | Input | PIN_76 | PIN_76 |
| A_7_0[5] | Input | PIN_77 | PIN_77 |
| A_7_0[4] | Input | PIN_79 | PIN_79 |
| A_7_0[3] | Input | PIN_80 | PIN_80 |
| A_7_0[2] | Input | PIN_81 | PIN_81 |
| A_7_0[1] | Input | PIN_84 | PIN_84 |
| A_7_0[0] | Input | PIN_4 | PIN_4 |
| AD_18_14[4] | Output | PIN_51 | PIN_51 |
| AD_18_14[3] | Output | PIN_50 | PIN_50 |
| AD_18_14[2] | Output | PIN_49 | PIN_49 |
| AD_18_14[1] | Output | PIN_48 | PIN_48 |
| AD_18_14[0] | Output | PIN_46 | PIN_46 |
| D_6_0[6] | Input | PIN_20 | PIN_20 |
| D_6_0[5] | Input | PIN_21 | PIN_21 |
| D_6_0[4] | Input | PIN_22 | PIN_22 |
| D_6_0[3] | Input | PIN_24 | PIN_24 |
| D_6_0[2] | Input | PIN_25 | PIN_25 |
| D_6_0[1] | Input | PIN_27 | PIN_27 |
| D_6_0[0] | Input | PIN_28 | PIN_28 |
| MDIS | Output | PIN_6 | PIN_6 |
| nCS0 | Output | PIN_52 | PIN_52 |
| nCS1 | Output | PIN_54 | PIN_54 |
| nCS2 | Output | PIN_55 | PIN_55 |
| nCS3 | Output | PIN_56 | PIN_56 |
| nENRAM | Input | PIN_64 | PIN_64 |
| nIOREQ | Input | PIN_12 | PIN_12 |
| nM1 | Input | PIN_10 | PIN_10 |
| nMREQ | Input | PIN_15 | PIN_15 |
| nPCW256k | Input | PIN_63 | PIN_63 |
| nRESET | Input | PIN_1 | PIN_1 |
| nWR | Input | PIN_17 | PIN_17 |
| PBit_7_3[4] | Input | PIN_57 | PIN_57 |
| PBit_7_3[3] | Input | PIN_58 | PIN_58 |
| PBit_7_3[2] | Input | PIN_45 | PIN_45 |
| PBit_7_3[1] | Input | PIN_60 | PIN_60 |
| PBit_7_3[0] | Input | PIN_61 | PIN_61 |
| TCK | Input | PIN_62 | PIN_62 |
| TDI | Input | PIN_14 | PIN_14 |
| TDO | Output | PIN_71 | PIN_71 |
| TMS | Input | PIN_23 | PIN_23 |
| | | | |
| | | | |